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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/682,863	10/25/2001	Donald Thomas McGrath	RD-27645	9978
23465	7590 01/16/2002			
JOHN S. BEULICK C/O ARMSTRONG TEASDALE, LLP ONE METROPOLITAN SQUARE			EXAMINER	
			SHINGLETON, MICHAEL B	
SUITE 2600	UITE 2600 T LOUIS, MO 63102-2740 ART UNIT PAPER		PAPER NUMBER	
51 E0015, W	0 03102 2710		2817	
			DATE MAILED: 01/16/2002	!

Please find below and/or attached an Office communication concerning this application or proceeding.

Applicant(s)	
2,863 McGrath	
Group Art Unit	
SHINGLETON 2817	
ver sheet beneath the correspondence address—	
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RE ONE MONTH(S) FROM THE	
event, however, may a response be timely filed after SIX (6) MONT thin the statutory minimum of thirty (30) days will be considered tim (6) MONTHS from the mailing date of this communication. see the application to become ABANDONED (35 U.S.C. § 133).	
atters, prosecution as to the merits is closed in 153 O.G. 213.	
is/are pending in the application.	
is/are withdrawn from consideration.	
is/are allowed.	
is/are rejected.	
is/are objected to.	
are subject to restriction or election	
requirement.	
TO-948.	
approved disapproved.	
e Examiner.	
C. § 11 9(a)-(d). documents have been	
ureau (PCT Rule 1 7.2(a)).	
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□ Interview Summary, PTO-413	

Application/Control Number: 09/682,863

Art Unit: 2817

Restriction to one of the following inventions is required under 35 U.S.C. 121:

I. Claims 1-5 and 10-24, drawn to amplifier system and method, classified in class330, subclass 9.

II. Claims 6-9, drawn to a buffered logic level shifting circuit, classified in class 327, subclass 333.

Inventions I and II are related as combination and subcombination. Inventions in this relationship are distinct if it can be shown that (1) the combination as claimed does not require the particulars of the subcombination as claimed for patentability, and (2) that the subcombination has utility by itself or in other combinations (MPEP § 806.05(c)). In the instant case, the combination as claimed does not require the particulars of the subcombination as claimed because the combination claims fail to set forth the first and second outputs of the buffered logic level shifting circuit. The subcombination has separate utility such as use in logic circuits that do not employ amplifiers.

Because these inventions are distinct for the reasons given above and have acquired a separate status in the art as shown by their different classification, restriction for examination purposes as indicated is proper.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Michael B. Shingleton whose telephone number is 703-308-4903. The examiner can normally be reached on Monday-Thursday from 8:30 to 4:30. The examiner can also be reached on alternate Fridays.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal, can be reached on (703) 308-4909. The fax phone number for the organization where this application or proceeding is assigned is 703-308-7722.

Application/Control Number: 09/682,863

Art Unit: 2817

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.

MBS

January 14, 2002

MICHAELBSHINGLETON PRIMARY EXAMINER

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